

# CALL FOR PAPERS

IEEE 8<sup>th</sup> International Symposium on Embedded Multicore/Many-core Systems-on-Chip



# MCSoc-14

The University of Aizu, Aizu-Wakamatsu, Japan  
September 23-25, 2014



Sponsored by IEEE and the University of Aizu

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## Symposium Overview

The IEEE 8th International Symposium on Embedded Multicore/Many-core Systems-on-Chip aims at providing the world's premier forum of leading researchers in the embedded Multicore/Many-core SoCs software, tools and applications design areas for Academia and industries. Prospective authors are invited to submit paper of their works.

Submission of a paper implies that at least one of the authors will have a full registration to the symposium upon acceptance of the paper.

## Topic of Interests

**Embedded Multicore/Many-core Programming:** Compilers, cross assemblers, Programming models, memory management, object-oriented aspects, concurrent software.

**Embedded Multicore/Many-core Architectures:** Multicore, Many-core, re-configurable platforms, memory management support, communication, protocols, real-time systems, SoCs and DSPs.

**Embedded Multicore/Many-core Design:** Hardware specification, modeling, synthesis, low power simulation and analysis, reliability, performance modeling, security issues.

**Embedded Multicore/Many-core Interconnection Networks:** Electronic/Photonic/RF NoC architectures, Power and energy issues in NoCs, Application specific NoC design, Timing, Synchronous/asynchronous communication, RTOS support for NoCs, Modeling, simulation, NoC support for MCSoc, NoC for FPGAs and structured ASICs, NoC design tools, Photonic components, Virtual fabrications, Photonic circuits, Routing, Filter design.

**Embedded Multicore/Many-core Testing:** Design-for-test, Test synthesis, Built-in-self-test, Embedded test for MCSoc.

**Embedded Multicore/Many-core Packaging Technologies:** 3D VLSI packaging Technology, Vertical Interconnections in 3D Electronics, Periphery Interconnection between Stacked ICs, Area Interconnection between Stacked ICs, Thermal management schemes.

**Embedded Multicore/Many-core Real-Time Systems:** real-time system design, RTOS, Compilation techniques, Memory/cache optimization, Interfacing and software issues, Distributed real-time systems, real-time kernels, Task scheduling, Multitasking design.

**Embedded Multicore/Many-core Benchmarks:** Parallel Benchmarks, Workload characterization and evaluation.

**Embedded Multicore/Many-core Applications:** Bio-medical, Aviation, Automobile, Military, Consumer electronics, and Novel applications.

## Important Dates

Full papers submission : March 31, 2014 ---> **April 30, 2014 (Extended)**  
Acceptance notification : May 31, 2014  
Camera ready paper : June 30, 2014  
Symposium : September 23-25, 2014

## Proceedings Publication and Indexing

MCSoc-14 proceedings will be published by IEEE CS Press, which will be included in the Computer Society Digital Library CSDL and IEEE Xplore. All CPS conference publications are also submitted for indexing to EI's Engineering Information Index, Compendex, and ISI Thomson's Scientific and Technical Proceedings, ISTP/ISI Proceedings, and ISI Thomson

For more information, please visit:

<http://www.mcsoc-forum.org/>



## Contact

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