

# SPECIAL SESSION ON

## Reconfigurable System on Chip (RSoC 2016)

Advances in chip design technology permit increasingly complex applications to be realized using reconfigurable systems-on-chips (RSOCs). Multi-processor architectures are a promising solution to provide the required computational performance for applications in the area of high performance computing. Reconfigurable multiprocessor systems are a particular type of embedded system, implemented using reconfigurable hardware. RSoCs workshop address advanced topics in system design and reconfigurable architecture. The workshop is organized in conjunction with the IEEE 10th International Symposium on Embedded Multicore/Many-core Systems-on-Chip (MCSoc-16), Lyon Congress Center, Lyon, France.. Authors are invited to submit high quality papers representing original work from both the academia and industry in (but not limited to) the following topics focusing on reconfigurable systems-on-chips:

- Reconfigurable System-on-Chip Design
- High Performance Reconfigurable System-on-Chip
- Reconfigurable Embedded Systems
- FPGAs-based Reconfigurable Systems-on-Chip
- Applications for Reconfigurable Systems-on-Chip in Telecommunication Networks
- Modeling Reconfigurable Systems-on-Chips
- Reconfigurable Multiprocessor Systems
- Reconfigurable network-on-chip Architecture
- Partial and Dynamic Reconfigurable FPGAs
- System Level Design of Reconfigurable Systems-on-Chip
- Hardware Security for Reconfigurable Systems-on-Chip
- Advanced Reconfigurable Computing

### **Important dates:**

Paper submission: ~~March 11, 2016~~ **April 30, 2016.**

Acceptance notification: June 24, 2016

Camera ready due: July 15, 2016

Symposium: September 21-23, 2016

### **Session Chair:**

Dr. Wael M El-Medany, University Of Bahrain, Bahrain  
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## Submission guidelines:

Submissions must be in PDF and should not exceed 8 pages. The submission must adhere to the two-column IEEE style using 10 pt. fonts. The page limit includes all figures and references. All pages should be numbered. Please make use of the following link to help you in the preparation and submission of your final manuscript: <http://www.computer.org/portal/web/cscps/submission>

**Online submission page:** <https://edas.info/newPaper.php?c=21730&track=79336>

## Publication:

Proceedings will be published by the IEEE CPS in the same volume of the main track. Authors of accepted papers are expected to register and present their papers at the symposium. Symposium proceedings will be included in the Computer Society Digital Library CSDL and IEEE Xplore. All CPS conference publications are also submitted for indexing to EI's Engineering Information Index, Compendex, and ISI Thomson's Scientific and Technical Proceedings, ISTP/ISI Proceedings, and ISI Thomson.

Authors of selected papers will be invited to submit extended article versions to [The International Journal of Computing and Digital Systems \(IJCDS\)](#)



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