CALL FOR PAPERS

IEEE 10th International Symposium on Embedded Multicore/Many-core Systems-on-Chip

Lyon Convention Center, France
September 21-23, 2016

Sponsored by IEEE. Technically supported by Ecole Centrale de Lyon and University of Aizu.

Symposium Overview

The IEEE 10th International Symposium on Embedded Multicore/Many-core Systems-on-Chip aims to provide world’s premier forum of leading researchers in the embedded Multicore/Many-core SoCs software, tools and applications design areas for academia and industries. Prospective authors are invited to submit their contributions. Submission of a paper implies that at least one of the authors will have a full registration to the symposium upon acceptance of the paper.

Topic of Interests

Programming: Compilers, automatic code generation methods, cross assemblers, programming models, memory management, runtime management, object-oriented aspects, concurrent software.

Architectures: Multicore/Many-core re-configurable platforms, memory management support, communication, protocols, real-time systems, SoCs and DSPs, heterogeneous architectures with HW accelerators and GPUs.

Design: Hardware specification, modeling, synthesis, low power simulation and analysis, reliability, variability compensation, thermal aware design, performance modeling, security issues.

Interconnection Networks: Electronic/Photonic/RF NoC architectures, Power and energy issues in NoCs, Application specific NoC design, Timing, Synchronous/asynchronous communication, RTOS support for NoCs, modeling, simulation, NoC support for MCM, NoC for FPGAs and structured ASICs, NoC design tools, photonic components, virtual interconnections, photonic circuits, routing, filter design.

Testing: Design-for-test, test synthesis, built-in-self-test, embedded test for MCSoC.

Packaging Technologies: 3D VLSI packaging technology, vertical interconnections in 3D Electronics, periphery interconnection between stacked ICs, area interconnection between stacked ICs, thermal management schemes.

Real-Time Systems: real-time system design, RTOS, compilation techniques, memory/cache optimization, interfacing and software issues, distributed real-time systems, real-time kernels, task scheduling, multitasking design.

Benchmarks: Parallel benchmarks, workload characterization and evaluation.

Applications: Bio-medical, health-care, computational biology, Internet of Things, smart mobility, electric vehicles, aviation, automobile, military, consumer electronics, and novel applications.


Keynotes

- David Atienza, École Polytechnique Fédérale de Lausanne
- Andreas Hansson, ARM
- Pascal Vivet, CEA-Leti, France
- Benoît Dupont de Nicechin, Kalray
- Hoi-Jun Yoo, KAIST

Proceedings Publication and Indexing

MCSoC-16 proceedings will be published by IEEE CS Press, which will be included in the Computer Society Digital Library CSDL and IEEE Xplore. All CPS conference publications are also submitted for indexing to EI’s Engineering Information Index, Compendex, and ISI Thomson’s Scientific and Technical Proceedings, ISTP/ISI Proceedings, and ISI Thomson.

Important Dates

Paper submission: April 30, 2016 (extended)
Acceptance notification: June 24, 2016
Camera ready paper: July 15, 2016
Symposium: September 21-23, 2016

Contact:
For any questions, please contact the Secretariat:
mcso16@mcsoc-forum.org

For more information please visit:
http://www.mcsoc-forum.org/

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- Ruichi Oka, President of UoA, JP

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