The IEEE 14th International Symposium on Embedded Multicore/Many-core Systems-on-Chip (MCSoC-2020) aims to provide world’s premier forum of leading researchers in the Embedded Multicore/Many-core SoCs software, tools and applications design areas for academia and industries. From the 2018 edition, the MCSoC symposium targets new emerging topic related to multicore neuro-inspired computing architectures and systems. Prospective authors are invited to submit their contributions. Submission of a contribution implies that at least one of the authors will have a full registration to the symposium upon acceptance of his/her contribution. Submission can include, technical and experimental, theoretical, conceptual, or a survey.

Topic of Interests

- Multi/Many-core Programming
- Multi/Many-core Architectures
- Multi/Many-core Design
- Interconnection Networks
- Multi/Many-core Testing
- Low-power Design
- System Security
- Real-Time Systems
- Embedded Applications
- Algorithms and Hardware for Learning On-chip
- Embedded Neuromorphic Systems

Proceedings Publication and Indexing

MCSoC-2020 proceedings will be published by IEEE CS Press, which will be included in the Computer Society Digital Library CSDL and IEEE Xplore. All CPS conference publications are also submitted for indexing to EI’s engineering Information Index, Compendex, and ISI Thomson’s Scientific and Technical Proceedings, ISTP/ISI Proceedings, and ISI Thomson.

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