

MCSoc

18th IEEE

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****See the Website for more details**

<https://www.mcsoc-forum.org>

2nd Round Paper Submission & Deadlines

- Paper submission: **October 15, 2025 (FIRM)**
- Acceptance notification: October 31, 2025
- Camera-ready paper: November 8, 2025
- Conference date: December 15-18, 2025

Submission System: <https://edas.info/N33173>

2025

Newcastle Australia Institute of Higher Education, Singapore, Dec. 15-18, 2025



The 18th IEEE International Symposium on Embedded Multicore/Many-core Systems-on-Chip (MCSoc-2025) aims to provide the world's premier forum of leading researchers in the Embedded Multicore/Many-core SoCs software, tools, and applications design areas for academia and industries.

Prospective authors are invited to submit their contributions. Submission of a contribution implies that at least one of the authors will have full registration to the symposium upon acceptance of their contribution. Submission can include technical, experimental, theoretical, conceptual, or a survey. ****The conference is a hybrid event****

Technical Tracks

- Embedded Multicore/Manycore SoC Programming
Chair: Trong-Thuc Hoang (UEC, Japan)
- Embedded Multicore/Manycore SoC Architectures
Chair: Man Wu (Keio University, Japan)
- Embedded Multicore/Manycore SoC Design
Chair: Cristinel Ababei (Marquette Univ., U.S.A)
- Embedded Multicore/Manycore SoC Interconnection Networks
Chair: Baris Taskin (Drexel University, U.S.A)
- Embedded Multicore/Manycore SoC Systems Testing, Security and Trust
Chair: ZHENG Yue (The Chinese Univ. of Hong Kong, Shenzhen, China)
- Embedded Multicore/Manycore SoC Design Automation and Low-power Design
Chair: Stefan Holst (Kyushu Institute of Technology, Japan)
- Embedded Multicore/Manycore SoC Real-Time Systems
Chair: Yi-Chung Chen (Mediatek, U.S.A)
- Operating Systems Platforms for Real-Time Embedded Applications
Chair: Lei Yang (George Mason University, U.S.A)
- Embedded Multicore/Manycore SoC Applications
Chair: Chai-Chi Tsai (National Cheng Kung University, Taiwan)
- Embedded Hardware Acceleration of AI on the Edge
Chairs: Shaswat Shresthamali (Keio Univ., Japan), Po-Tsang Huang (NYCU, Taiwan)
- ML for Energy-Efficient, High-Performance, and Reliable Manycore Systems and Interconnects
Chairs: Md. Farhadur Reza (Eastern Illinois University, U.S.A), Hanzhi Ma (Zhejiang Univ., China)
- Chiplet-based Multicore Architecture and Design
Chairs: Jason Eshraghian (UCSC, U.S.A), Darshika G. Perera (UCCS, U.S.A)

Special Sessions

- Performance Optimization & Auto-Tuning of Software on Multicore/Manycore Systems
Chair: Tetsuya Hoshino, Nagoya University, Japan
- Emerging Machine Learning and Deep Learning Models: Theory & Applications
Chair: Jungpil Shin (The University of Aizu, Japan)
- Embedded Neuromorphic Computing Systems
Chair: Khanh Dang (The University of Aizu, Japan)
- Embedded Applications and Ubiquitous Computing
Chair: Taihai Chen (Zhejiang University, China)
- Embedded Machine Learning and Data Analytics
Chairs: Kasem Khalil (University of Mississippi, USA), Qinglin Yang (SYSU, China)
- Embedded, Cyber-Physical, and IoT Systems
Chair: Bahar Farahani (Shahid Beheshti University, Iran)
- Embedded Biomedical Engineering
Chairs: Xin Zhu (Institute of Science Tokyo, Japan), Md Chowdhury (Qatar University, Qatar)
- Machine Learning and Neuromorphic Computing for Edge and IoT
Chair: Anh Vu Doan (Infineon, Germany)
- Parallel/Distributed, Grid, and Cloud Computing
Chairs: Lingjun Zhao (GPNV, China), Zhishang Wang (The University of Aizu, Japan)
- Intelligent Mechatronics & Neuroprosthesis Technologies
Chair: Chun-Ming Huang (Taiwan Semiconductor Research Institute, Taiwan)
- Distributed Computing & Comm. Techniques for Emerging AI Applications
Chair: Haoli Zhao (Hebei University of Technology, China)
- Quantum Technology and Machine Learning
Chair: Deepika Saxena (The University of Aizu, Japan)
- Real-Time Natural Language Processing
Chairs: Mudar Sarem (Manara University, Syria), Xiaoming He (NJUPT, China)
- Optical Communications, Devices and Networking
Chair: Guo-Wei Lu (Kyushu University, Japan)
- Advances in Intelligent Systems and Learning Technologies
Chairs: Mohamed Hamada (The Univ. of Aizu, Japan)
- Special Session on Cutting-Edge Developments in Emerging Applications
Chair: Chiang Liang Kok, University of Newcastle, Australia

Proceedings Publication and Indexing

The conference proceedings, including all technical tracks, special tracks, and special sessions and workshop papers, will be published by IEEE Conference Proceedings, which will be included in the Computer Society Digital Library CSDL and IEEE Xplore. All CPS conference publications are also submitted for indexing to EI's Engineering Information Index, Compendex, ISI Thomson's Scientific and Tech. Pro. Scopus, ISTP/ISI Proceedings.