



# 19<sup>th</sup> IEEE

# MCSOC 2026



Shanghai Jiao Tong University, Shanghai, China, Dec. 14-17, 2026

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# Call for Special Sessions

The Organizing Committee of the **19th IEEE International Symposium on Embedded Multicore/Many-core Systems-on-Chip (MCSOC 2026)** invites proposals for **Special Sessions** to be held during the symposium. Special Sessions aim to enrich the technical program by spotlighting emerging research directions, disruptive technologies, and innovative applications in the broad domains of **multicore/many-core architectures, on-chip systems, AI accelerators, intelligent edge platforms, and next-generation computing paradigms**.

Proposals should focus on timely, high-impact topics that advance the state of the art and stimulate deeper interaction among researchers, practitioners, and industry partners. Sessions that strengthen **academic-industrial collaboration**, promote **cross-disciplinary innovation**, or highlight **regional and global research initiatives** are especially welcome.

Special Session proposals in all areas relevant to MCSOC — including architecture, design methodologies, compilers, runtime systems, AI/ML hardware, embedded systems, reconfigurable computing, and emerging application domains — are encouraged. Inter-society proposals jointly organized with other IEEE Societies (beyond the Computer Society and TCMM) are particularly encouraged, as they broaden the symposium's reach and foster interdisciplinary collaboration.

All papers submitted to Special Sessions will undergo the **same rigorous peer-review process** as regular submissions.

Special Session proposals (maximum **3 pages**) should include the following information:

- **Title and Abstract:** Provide a clear and concise title along with an abstract describing the session's theme, scope, and expected impact within the MCSOC community.
- **Rationale:** Explain why the proposed topic is novel, timely, and relevant to the **multicore/many-core, SoC, AI accelerator, and intelligent systems** communities. Describe how the session will enrich the MCSOC 2026 technical program and its anticipated contribution to advancing research and industry engagement.
- **Proposed Papers and Speakers:** Include a list of proposed papers with tentative titles, authors, and affiliations

Prospective organizers are invited to submit their proposals to the IEEE MCSOC 2026 Special Session Chair: [mcsoc.forum@gmail.com](mailto:mcsoc.forum@gmail.com)

**Special session proposal due date: May 30, 2026**

## Proceedings Publication and Indexing

The conference proceedings, including all technical tracks, special tracks, special sessions, and workshop papers, will be published by IEEE Conference Proceedings, which will be included in the Computer Society Digital Library CSDL and IEEE Xplore. All CPS conference publications are also submitted for indexing to EI's Engineering Information Index, Compendex, ISI Thomson's Scientific and Tech. Pro. Scopus, ISTP/ISI Proceedings.

## Paper Submission & Deadlines

- Paper submission: **April 30, 2026**
- Acceptance notification: July 20, 2026
- Camera-ready paper: July 31, 2026
- Conference date: December 14-17, 2026

Submission System: <https://edas.info/N34632>

<https://www.mcsoc-forum.org>

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MULTICORE/MANY-CORE SYSTEMS-ON-CHIP

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