

Call for Papers

Special Session on Novel Architectures and Design Tools for Low-Power High-Performance Computing Systems

We are living in the Information Age. Many human activities are fundamentally changed by Information Freedom that everyone can gather, access, store, process, and share information anytime and anywhere. The capability to collect and utilize information is an increasingly important social and economic power for modern societies, and computing systems are at the heart of the information systems serving us. Thanks to the continuous advancements of semiconductor technologies, architecture innovations, and design tools, an increasingly large number of transistors can be integrated on a single chip. A major challenge is to achieve high energy efficiency and performance potentials at the same time.

This special session tries to highlight the latest findings and innovations in the architectures, design tools and technologies, and methodologies for low-power high-performance computing systems. We invite authors to submit high-quality works in related areas including but not limited to:

- Many-core/multi-core architectures
- Heterogeneous architectures
- Reconfigurable architectures
- Network-on-chips and inter-chip interconnect networks
- Tools for many-core/multi-core architecture explorations
- Tools for reconfigurable architecture studies
- Tools for inter/intra-chip interconnect network designs
- Case studies

Important dates:

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|--------------------------|---------------|
| Paper submission: | 2015-03-31 |
| Acceptance notification: | 2015-06-22 |
| Camera ready: | 2015-06-30 |
| Symposium: | 2015-09-23~25 |

Session Chairs:

Zuocheng Xing, National University of Defense Technology

Jiang Xu, Hong Kong University of Science and Technology