



**Singapore University of Technology and Design,
Dec 20-23, 2021**

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The IEEE 14th International Symposium on Embedded Multicore/Many-core Systems-on-Chip (MCSoc-2021) aims to provide world's premier forum of leading researchers in the Embedded Multicore/Many-core SoCs software, tools and applications design areas for academia and industries. From the 2018 edition, the MCSoc symposium targets new emerging topic related to multicore neuro-inspired computing architectures and systems. Prospective authors are invited to submit their contributions. Submission of a contribution implies that at least one of the authors will have a full registration to the symposium upon acceptance of his/her contribution. Submission can include, technical and experimental, theoretical, conceptual, or a survey.

Technical Tracks

- Embedded Multicore/Manycore SoC Programming
Track Chair: Stéphane Louise (CEA, LIST, France)
- Embedded Multicore/Manycore SoC Architectures
Track Chair: William J. Song (Yonsei Univ, Korea)
- Embedded Multicore/Manycore SoC Design
Track Chair: Diana Göhringer (TUD, Germany), Cristinel Ababei (Marquette Univ, USA)
- Embedded Multicore/Manycore SoC Interconnection Networks
Track Chair: José L. Abellán (UCAM, Spain)
- Embedded Multicore/Manycore SoC Testing
Track Chair: Yeong-Kang Lai (NCHU, Taiwan)
- Embedded Multicore/Manycore SoC Design Automation and Low-power Design
Track Chair: Fakhrol Z. Rokhani (UPM, Malaysia), Mohamed M. Sabry (NTU, Singapore)
- Embedded Multicore/Manycore SoC Real-Time Systems
Track Chair: Yi-Chung Chen (SUNY, USA)
- Operating Systems Platforms for Real-Time Embedded Applications
Track Chair: Shinobu Miwa (UEC, Japan)
- Embedded Multicore/Manycore SoC Applications
Track Chair: Karol Desnos (INSA Rennes, France)
- Algorithms, Architecture and Hardware for AI
Track Chair: Lan-Da Van (NCTU, Taiwan)
- Embedded Neuromorphic Computing Systems
Track Chair: Charlotte Frenkel (UCLouvain, Belgium), Gianvito Urgese (PoliTO, Italy)
- Secure and Fault-Tolerant Embedded Computing
Track Chair: Chun-Ming Huang (TSRI, Taiwan)
- Machine Learning for Energy-efficient, Reliable Manycore Interconnects
Track Chair: Amey Kulkarni (NVIDIA, USA)

Special Sessions

- Auto-Tuning for Multicore and GPU (ATMG2021)
Session Chair: Masahiro Nakao (RIKEN R-CCS, Japan)
- FPGA Technologies for Adaptive Computing (FTAC 2021)
Session Chair: Fujieda Naoki (AIT, Japan)
- Distributed Computing and Communication Techniques for Emerging AI Applications
Session Chair: Peng Li (Univ. of Aizu, Japan),
- Secure, Reliable and Energy-efficient Execution on MPSoCs
Session Chair: Alok Prakesh (NTU, Singapore), Amit K. Singh (Univ. of Essex, UK)
- Intelligent Systems and Learning Technologies: Models, Methods, and Applications
Session Chair: Mohamed Hamada (Univ. of Aizu, Japan), Sarika Jain (NITKRR, India)
- Quantum Computation and Simulation
Session Chair: Wu Chunfeng (SUTD, Singapore), Kwek L. Chuan (NTU, Singapore)
- Low-power and Solutions for Future SoC design
Session Chair: Akram B. Ahmed (AIST, Japan), Hayate Okuhara (UNIBO, Italy)
- Emerging Machine Learning and Deep Learning Models: Theory and Applications
Session Chair: Jungpil SHIN (Univ. of Aizu, Japan)

Proceedings Publication and Indexing

MCSoc-2021 proceedings will be published by IEEE CS Press, which will be included in the Computer Society Digital Library CSDL and IEEE Xplore. All CPS conference publications are also submitted for indexing to EI's engineering Information Index, Compendex, and ISI Thomson's Scientific and Technical Proceedings, ISTP/ISI Proceedings, and ISI Thomson.

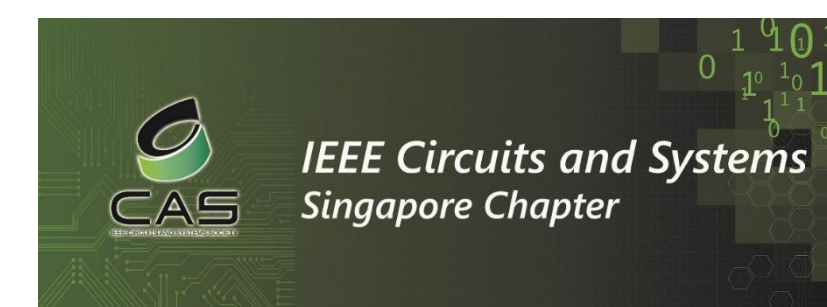
Important Dates

- Abstract submission: July 30, 2021
- **Full Paper submission: July 30, 2021 (extended)**
- Acceptance notification: August 30, 2021
- Camera-ready paper: September 15, 2021
- Symposium date: December 20-23, 2021

Submission System: <https://edas.info/N27016>

www.mcsoc-forum.org

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