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15th Edition

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Important Dates

- Abstract submission: May 30, 2022
- **Full Paper submission: June 5, 2022**
- Acceptance notification: July 31, 2022
- Camera-ready paper: August 15, 2022
- Symposium date: December 19-22, 2022

Submission System: <https://edas.info/N29397>

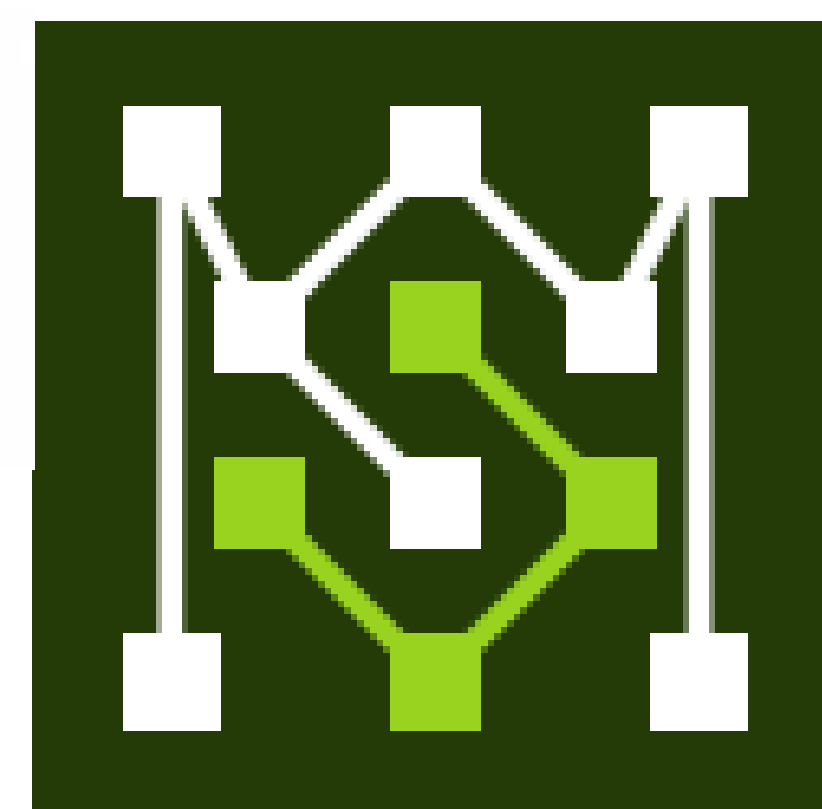
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The 15th edition of the International Symposium on Embedded Multicore/Many-core Systems-on-Chip (MCSoc-2022) aims to provide the world's premier forum of leading researchers in the Embedded Multicore/Many-core SoCs software, tools, and applications design areas for academia and industries. From the 2018 edition, the MCSoc symposium targets new emerging topics related to multicore neuro-inspired computing architectures and systems. Prospective authors are invited to submit their contributions. Submission of a contribution implies that at least one of the authors will have a full registration to the symposium upon acceptance of his/her contribution. Submission can include, technical, experimental, theoretical, conceptual, or a survey.

Technical Tracks

- Embedded Multicore/Manycore SoC Programming
Track Chair: Stéphane Louise (CEA, LIST, France)
- Embedded Multicore/Manycore SoC Architectures
Track Chair: William J. Song (Yonsei Univ, Korea)
- Embedded Multicore/Manycore SoC Design
Track Chair: Diana Göhringer (TUD, Germany), Cristinel Ababei (Marquette Univ, USA)
- Embedded Multicore/Manycore SoC Interconnection Networks
Track Chair: José L. Abellán (UCAM, Spain)
- Embedded Multicore/Manycore SoC Testing
Track Chair: Yeong-Kang Lai (NCHU, Taiwan)
- Embedded Multicore/Manycore SoC Design Automation and Low-power Design
Track Chair: Fakhrul Z. Rokhani (UPM, Malaysia)
- Embedded Multicore/Manycore SoC Real-Time Systems
Track Chair: Yi-Chung Chen (SUNY, USA)
- Operating Systems Platforms for Real-Time Embedded Applications
Track Chair: Tsung-Tai Yeh (NYCU, Taiwan)
- Embedded Multicore/Manycore SoC Applications
Track Chair:
- Hardware Acceleration of AI on the Edge
Track Chair: Po-Tsang Huang (NYCU, Taiwan)
- Embedded Neuromorphic Computing Systems
Track Chair: Gianvito Urgese (PoliTO, Italy)
- Secure and Fault-Tolerant Embedded Computing
Track Chair: Chun-Ming Huang (TSRI, Taiwan)
- Machine Learning for Energy-efficient, Reliable Manycore Interconnects
Track Chair: Kun-Chih Chen (NSYSU, Taiwan)

Special Tracks

- Embedded, Cyber-Physical, and IoT Systems
Session Chair: Xuchong Zhang (Xi'an Jiaotong University, China)
- Embedded, Applications and Ubiquitous Computing
Session Chairs: Chih-Peng Fan (NCHU, Taiwan)
- Embedded Neuroprosthesis, Technologies, and Devices
Session Chairs: Chun-Shu Wei (NYCU, Taiwan), Chul Kim (KAIST, Korea)
- Embedded Machine Learning and Data Analytics
Session Chair: Yuan Du (NJU, China)

Special Sessions

- Auto-Tuning for Multicore and GPU (ATMG2021)
Session Chair: Daichi Mukunoki (RIKEN, Japan)
- Distributed Computing and Communication Techniques for Emerging AI Applications
Session Chair: Peng Li (Univ. of Aizu, Japan),
- Intelligent Systems and Learning Technologies: Models, Methods, and Applications
Session Chair: Mohamed Hamada (Univ. of Aizu, Japan), Sarika Jain (NITKRR, India)
- Low-power and Solutions for Future SoC design
Session Chair: Akram B. Ahmed (AIST, Japan), Hayate Okuhara (UNIBO, Italy)
- Emerging Machine Learning and Deep Learning Models: Theory and Applications
Session Chair: Jungpil SHIN (Univ. of Aizu, Japan)
- Device, Circuit, Architecture, and CAD tools of Advanced FPGAs and Their Application to Edge AI Computing
Session Chair: Daisuke Suzuki (Univ. Aizu, Japan), Takahiro Hanyu (Tohoku Univ., Japan)

Proceedings Publication and Indexing

MCSoc-2022 proceedings will be published by IEEE CS Press, which will be included in the Computer Society Digital Library CSDL and IEEE Xplore. All CPS conference publications are also submitted for indexing to EI's engineering Information Index, Compendex, and ISI Thomson's Scientific and Technical Proceedings, ISTP/ISI Proceedings, and ISI Thomson.

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