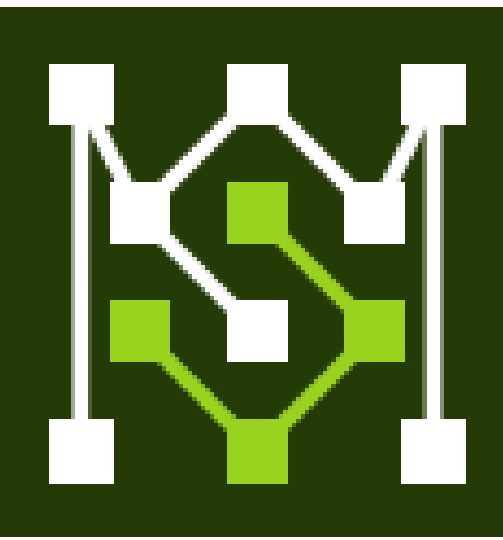


# MCSoc 18<sup>th</sup> IEEE

# 2025



Newcastle Australia Institute of Higher Education, Singapore,  
Dec. 15-18, 2025

The 18<sup>th</sup> IEEE International Symposium on Embedded Multicore/Many-core Systems-on-Chip (MCSoc-2025) aims to provide the world's premier forum of leading researchers in the Embedded Multicore/Many-core SoCs software, tools, and applications design areas for academia and industries.

Prospective authors are invited to submit their contributions. Submission of a contribution implies that at least one of the authors will have full registration to the symposium upon acceptance of their contribution. Submission can include technical, experimental, theoretical, conceptual, or a survey. **\*\*The conference is a hybrid event\*\***

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- Chiplet-based Multicore Architecture and Design  
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- Performance Optimization & Auto-Tuning of Software on Multicore/Manycore Systems  
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*Chairs: Mohamed Hamada (The Univ. of Aizu, Japan)*
- Special Session on Cutting-Edge Developments in Emerging Applications  
*Chair: Chiang Liang Kok, University of Newcastle, Australia*

## Proceedings Publication and Indexing

The conference proceedings, including all technical tracks, special tracks, and special sessions and workshop papers, will be published by IEEE Conference Proceedings, which will be included in the Computer Society Digital Library CSDL and IEEE Xplore. All CPS conference publications are also submitted for indexing to EI's Engineering Information Index, Compendex, ISI Thomson's Scientific and Tech. Pro. Scopus, ISTP/ISI Proceedings.

## Paper Submission & Deadlines

- Paper submission: **April 30, 2025**
- Acceptance notification: July 20, 2025
- Camera-ready paper: July 31, 2025
- Conference date: December 15-18, 2025

Submission System: <https://edas.info/N33173>

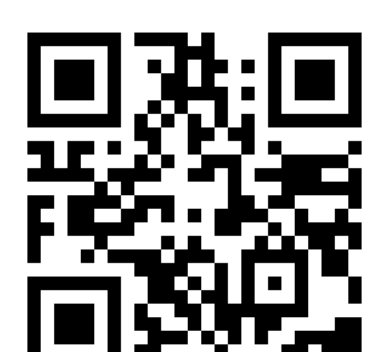
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